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FORM PTO-1449	U.S. Department of Commerce	Atty. Docket No.	Application No. Div. of 09/277,677
(Rev. 7-80)	Patent and Trademark Office	TESSERA 3.0-176 DIV	
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Beroz et al.

(Use several sheets if necessary)		Filing Date: Herewith			Group 2841						
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*EXAMINER INITIALS		DOCUMENT NUMBER	DATE	1	NAME	CLASS	SUBCLA	ss		G DATE IF PRIATE	
BT	AA	5,148,265	09/1992	Khandros e	t al.	357	80				
	AB	5,148,266	09/1992	Khandros e	t al.	357	80				
	AC	5,801,446	09/1998	DiStefano	et al.	257	778				
	AD	5,519,580	05/1996	Natarajan e	t al.	361	760				_
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	ΑI	3,530,229	09/1970	Burke, Jr.		174	68.5				
<u> </u>	AJ	5,818,697	10/1998	Armezzani	et al.	361	749				
BT	AK	3,360,852	01/1968	Cochran		29	624				
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BT	AL	WO 95/34106	12/1995	WIPO							
BÍ	AM	WO 97/44859	11/1997	WIPO							
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	АО										
	AP								·		
		OTHER PRIOR A	ART (Inclu	ding Author,	Fitle, Date, Pertin	ent Pages,	Etc.)		•		
BT	AR Andros and Hammer, "TBGA Package Technology" IEEE Transactions on Components, Packaging and Manufacturing Technology, Part B, Vol. 17, No. 4, VP 564-568.										
B	AS	Lau, John H., Ball Grid A	Lau, John H., Ball Grid Array Technology, pp. 460-464.								
Publication,in American National Standard ANSI/IPC-D-249, "Design Standard for Flexible Single and Double Sided Printed Boards," January 1997, p. 47.											
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B. Talba

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 U.S. Department of Commerce (Rev. 7-80) Patent and Trademark Office			Atty. Docket No. TESSERA 3.0-176 DIV			Application No. Div. of 09/277,677					
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35	AC	4,616,292	10/1986	Sengoku e	t al.	361	414				
	AD										
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185	AR	American National Stand Boards and Rigid Printed				ntitled Des	ign Stand	ard fo	or Rigid F	rinted	
B	AS	Design Guidelines for Surface Mount and Fine Pitch Technology, 2nd edition, 1996 by Vernon Solberg, pp.142-143.									
BT	ΑТ	Fjelstad, An Engineer's Guide to Flexible Circuit Technology (ElectroChemical Publications Limited 1997; ISBN 0901150347), pp.148-149.									
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BT	AR	Standard ANSI/IPC-D-3	50, Revisi	on D, July 1992.							
B	AS	IBM Technical Disclosure Bulletin, Vol. 40, No. 6, pp. 199-200 entitled "Ball Grid Array Solder Ball on Ball Grid Array Dimple Pad" (June 1997).									
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